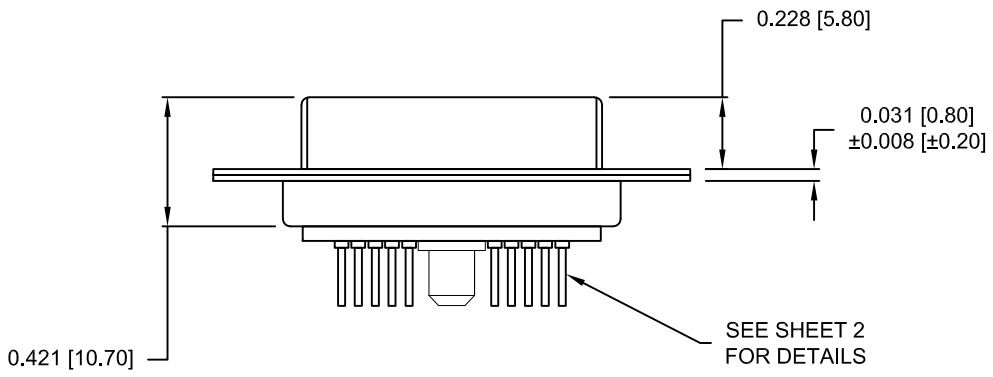
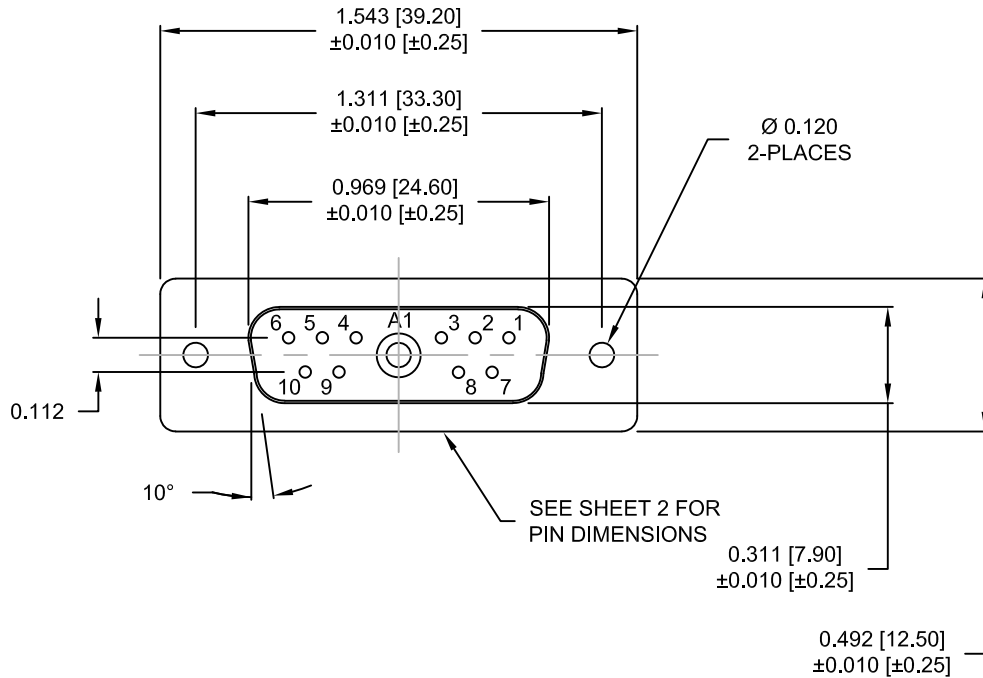


DESCRIPTION: POWER / SIGNAL - VERTICAL BOARD - FEMALE - IP67



**SPECIFICATIONS:**

**ELECTRICAL:**

INSULATOR RESISTANCE: 5 G OHMS Min.  
 DIELECTRIC WITHSTANDING: 1,000VAC r.m.s.  
 OPERATING TEMPERATURE: -55° C ~ +125° C  
 POWER CONTACT RESISTANCE: 1.0 mOHMS Max.  
 SIGNAL CONTACT RESISTANCE: 10 mOHMS Max.  
 POWER PINS: 40 AMPS  
 SIGNAL PINS: 5 AMPS

**MECHANICAL:**

CONTACT INSERTION FORCE: 4.5 Kg Max.  
 CONTACT SEPARATOR FORCE: 0.3Kg Min.

**MATERIALS:**

CONTACTS: COPPER ALLOY  
 INSULATOR: PBT UL94V-0 RATED, BLK  
 230°C PROCESS TEMP.  
 SHELL: STEEL  
 SEAL: F04 FLEXIBLE ADHESIVE

**FINISH:**

SHELL: NICKEL  
 CONTACTS: FULL GOLD FLASH

**681S11W1203LYYY**

0 = 40 AMP  
 0 = NO HARDWARE  
 4 = 0.237" BOARDLOCK PCB SIDE  
 1 = GOLD FLASH

**MEETS IP67 STANDARDS**

(SEE PAGE 2 FOR SEALING DETAILS)

**RoHS COMPLIANT**

UNITS = INCHES [MM] **DO NOT SCALE FROM DRAWING**



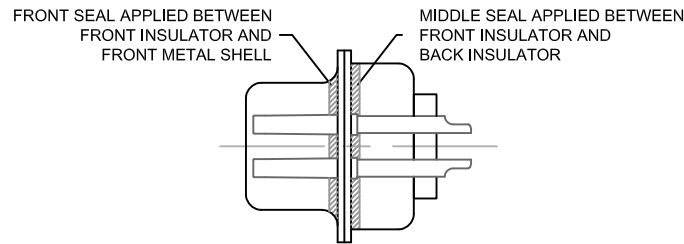
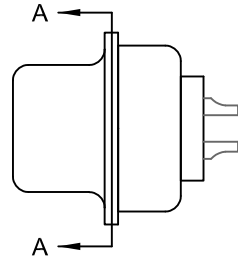
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DRAWN: M. SIGMON  
 DATE: 06/11/2013

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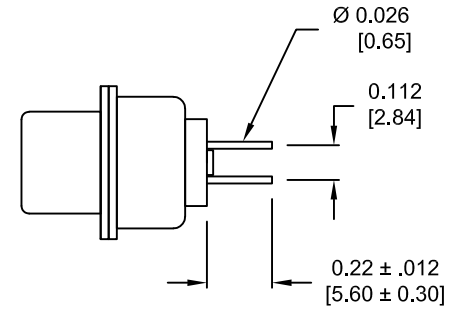
SCALE: NTS	SHEET 1	OF 4	REV 1
<b>681S11W1203LYYY</b>			

DESCRIPTION: 681 SERIES CONTACTS

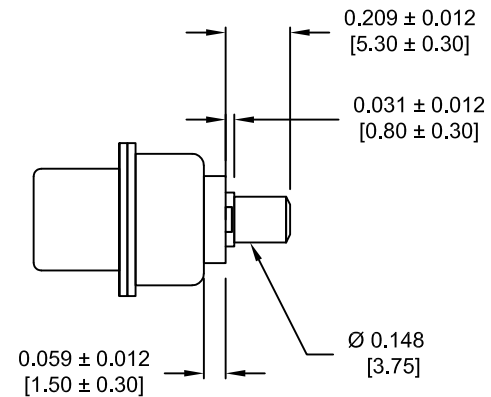


DETAIL A-A  
SHOWN FOR REFERENCE ONLY

NOTE:  
SIGNAL AND POWER CONTACTS  
COME PRE-LOADED INTO INSULATORS



SIGNAL CONTACTS



POWER CONTACTS

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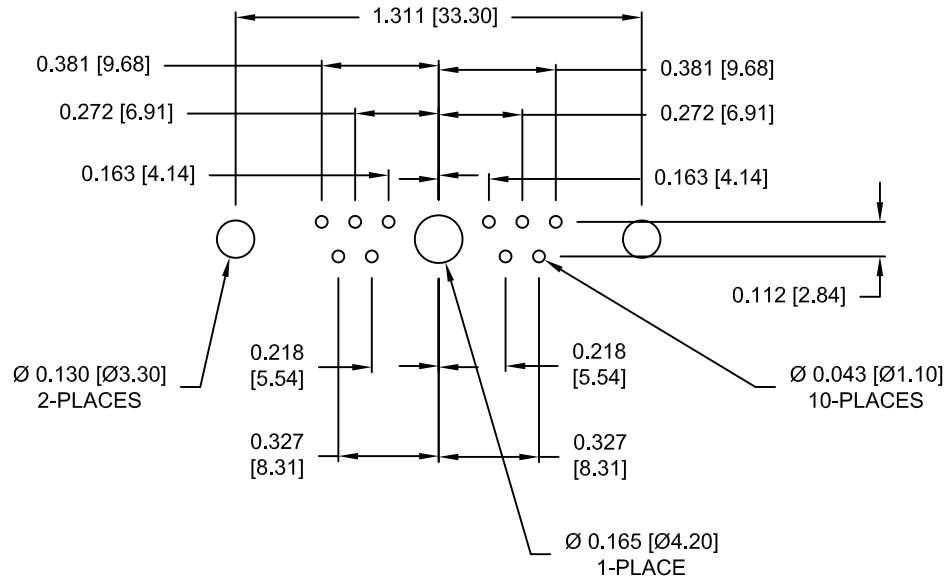
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M. SIGMON

DATE:  
06/11/2013

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SCALE: NTS	SHEET 2	OF 4	REV 1
681S11W1203LYYY			

DESCRIPTION: PCB LAYOUT



RECOMMENDED PCB LAYOUT

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DATE:  
06/11/2013

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SCALE:  
NTS

SHEET OF  
3 4

REV  
1

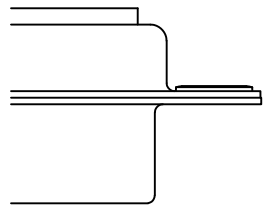
681S11W1203LYYY

DESCRIPTION: CLINCH NUT HARDWARE

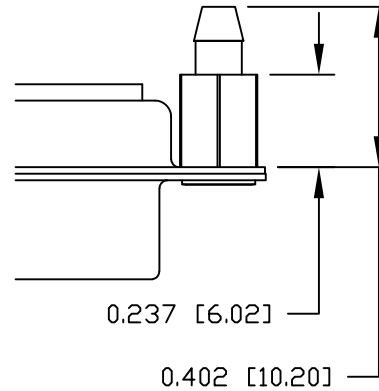
ALL CLINCH-NUTS ARE 4-40 INTERNAL THREADS

TOLERANCE:  
ALL DIMENSIONS ARE  $\pm 0.010$

0 ..... NO HARDWARE



4 ..... 0.237" PCB SIDE



RoHS COMPLIANT

UNITS = INCHES [MM] DO NOT SCALE FROM DRAWING



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SCALE:  
NTS

SHEET OF  
4 4

REV  
1

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