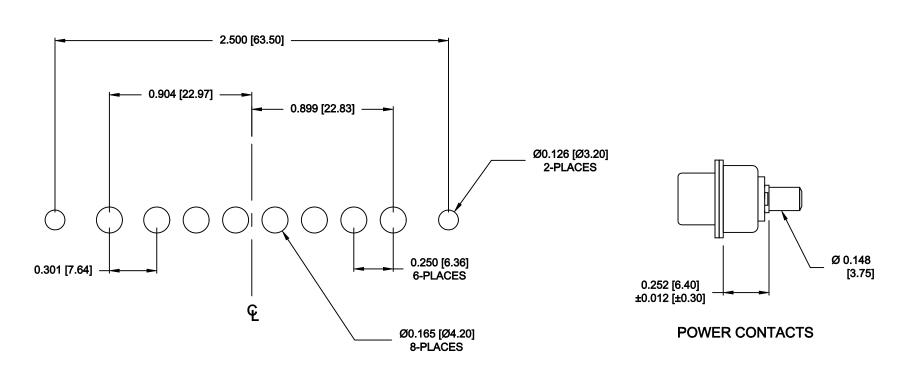


## NOTE: POWER CONTACTS COME PRE-LOADED INTO INSULATORS



RECOMMENDED PCB LAYOUT TOLERANCE ± 0.002 [0.05]

## ROHS COMPLIANT

